



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2122
Examiner : To be assigned

In Re PATENT APPLICATION Of:

Applicant : Chun Nan TSAI et al.
Serial No. : 09/805,578
Filed : March 14, 2001
For : PCI BUS CYCLE SINGLE-STEP
INTERRUPTION DEBUG CARD

STATUS REQUEST

Attorney Ref: TAIW 106

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please let us know the status of the above-identified application and when an Action may be expected. Please respond via facsimile or e-mail Tonya Harris (Docketing Manager) at tonya@rabinchamp.com.

Respectfully submitted,

Steven M. Rabin

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July 30, 2004
Date

SMR:dt

FEE ENCLOSED: \$
Please charge any further
fee to our Deposit Account
No. 10-0002